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FUNDAMENTALS OF SEMICONDUCTOR FABRICATION G. S. May Motorola Foundation Professor School of Electrical & Computer Engineering Georgia Institute of Technology Atlanta, GA, USA S. M. SZE UMC Chair Professor National Chiao Tung University National Nano Device Laboratories Hsinchu, Taiwan 1 John Wiley and Sons, Inc New York.

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MM5017: Electronic materials, devices, and fabrication Figure 3: Cross section of a MOSFET showing the different layers. Poly Si is used as gate with SiN<sub>x</sub> used as the interlayer dielectric. Layering is the process by which all of these different materials are added to the MOSFET. Adapted from Fundamentals of semiconductor manufacturing and process

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2 Semiconductor Fundamentals - 40 - conduction is more than the number of hole in valence band. Therefore, Fermi level of n-type semiconductor is closer to the band edge of conduction band. Likewise, for p-type semiconductor, the number of hole in valence is more than the number conduction band.

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Cit s, Spring 2007. MIT Op JV: 2.372J/6.777J Spring 2007, Lecture 7E - 5 Razeghi, Fundamentals of Solid State Engineering > The highest normally filled set of electronic states is the valence band > The lowest normally empty set of electronic states is the conduction band > An energy gap separates these states > At  $T=0$  K, all the valence band states are filled

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38 THE MANUFACTURING PROCESS Chapter 2 scalable nature of the optolithographical process is what makes the cheap manufacturing of complex semiconductor circuits possible, and lies at the core of the economic success of the semiconductor industry. The continued scaling of the minimum feature sizes in integrated circuits puts an

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ics. Silicon is the most important semiconductor and is the active material in almost all electronic devices. A few other semiconductors- for example, gallium arsenide- are es-sential because they can be used to make optoelectronic devices. We will focus on semiconductor silicon.

### **SEMICONDUCTOR DEVICES AND SAMPLE distribution TECHNOLOGY**

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59 3 Device Fabrication Technology1 CHAPTER OBJECTIVES While the previous chapters explain the properties of semiconductors, this chapter will explain how devices are made out of the semiconductors.

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THE NATURE OF DATA IN SEMICONDUCTOR MANU-FACTURING An overview of the variety of data utilized in semiconductor manufacturing helps to set the context for application of the statistical methods to be discussed here. Most data in a typical semiconductor fabrication plant (or "fab") consists of in-line and end-of-line data used for process ...

### **THE NATURE OF DATA IN SEMICONDUCTOR MANU- FACTURING**

complete fabrication processes of a semiconductor... Pride, lust, gluttony, greed, envy, sloth, and anger. They're considered "deadly" because of their capacity to generate other evils.

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1 PHOTOLITHOGRAPHY Theory 1. Introduction Optical lithography is the process of creating specific patterns on semiconductor wafers using photosensitive material (known as photoresist, PR) and an ultraviolet light exposure system to

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The availability of power semiconductor devices with high input impedance has encouraged the development of integrated control circuits. In general, the integration of the control circuit is preferred over the discrete counterpart due to reduced manufacturing costs at high volumes and improved reliability from a reduction of the interconnects.

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