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done in the context of a digital electronics lab, comparing the LabVIEW simulations with real integrated circuits. In each case, you can enhance simulations presented in the text by using a National Instruments DAQ board to interact with the real world through LabVIEW digital I/O, analog out, analog in, and serial VIs.

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Fundamentals of digital logic with Verilog design / Stephen D. Brown, Zvonko G. Vranesic. 1st ed. p. cm. (McGraw-Hill Series in electrical and computer engineering) Includes index. ISBN 0-07-282315-1 1. Logic circuits—Design and construction—Data processing. 2. Verilog (Computer hardware description language). 3. Computer-aided design. I.

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Fundamentals of Digital Logic with Verilog Design, Stephen

Fundamentals of Digital Logic with VHDL Design Book Description: Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits. The text provides a clear and easily understandable discussion of logic circuit design without the use of unnecessary formalism.

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